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## **Altera De2 Labs Solution**

This is my set of solutions to Altera's DE2 example labs. These were done as part of a course I took at university during the Fall of 2010. I am publishing this code because I found these labs challenging and had a hard time getting help on them.

**GitHub - BenBergman/AlteraDE2Labs\_Ver**

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**ilog: My solutions to**

...

Digital Labs using the Altera DE2 Board The Altera DE2 Board, featuring an an Altera Cyclone II®FPGA, offers varied technology suitable for a wide range of design projects.

## **Digital Labs using the Altera DE2 Board - John Loomis**

The DE2 series has consistently been at

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the forefront of educational development boards by distinguishing itself with an abundance of interfaces to accommodate various application needs. Extending its leadership and success, Terasic announces the latest DE2-115 that features the Cyclone® IV E device.

**Altera DE2-115**  
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## **Development and Education Board**

Altera Forum (Intel) asked a question. May 2, 2014 at 4:29 PM.  
DE2-115 lab exercises solutions. Dear all, I ordered a DE2-115 board. In the CD with board, there are ten lab sections which will be very beneficial for me to learn this FPGA based system. ... For lab exercise solutions, ...



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## **DE2-115 lab exercises solutions - Intel® Community Forum**

The Verilog 1 Lab from Altera is a rather lengthy lab for a beginner lab. From my estimates, a student should take several hours per day to finish it. I highly recommend reading Altera's Verilog Laboratory Exercise 1 before reading this article as I will go quickly through the

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solutions. This lab contains 6 parts that cover mid level logic design.

## **FPGA Project: Verilog Lab 1: Altera DE2 Board - Basic Verilog**

DE2 User Manual 4  
Chapter 2 Altera DE2  
Board This chapter  
presents the features  
and design  
characteristics of the  
DE2 board. 2.1 Layout  
and Components A

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photograph of the DE2 board is shown in Figure 2.1. It depicts the layout of the board and indicates the location of the connectors and key components. Figure 2.1. The DE2 board.

## **DE2 Development and Education Board User Manual**

Altera DE2 Board  
Resources for  
Students. click DE2  
image above to view

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larger image. How to purchase a DE2 board. New DE1 info is here. New Camera and LCD info is here DE2 Design Examples DE2 Clock is a clock/timer that uses the DE2's LCD to display the current time. A VHDL-based state machine is used to communicate with the LCD display controller.

## **Altera DE2 Board Resources**

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4. Create a new Quartus II project which will be used for implementation of the circuit on the Altera DE2 board. This project should consist of a top-level entity that contains the appropriate input and output ports for the Altera board. Instantiate your processor in this top-level entity. Use switches SW15–0 to drive the

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## **Laboratory Exercise 9**

Dear all, I ordered a DE2-115 board. In the CD with board, there are ten lab sections which will be very beneficial for me to learn this FPGA based system. The lab sections only contains task, but there is no design solution files. Could anyone know where I could get the design solution ...

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## **DE2-115 lab exercises solutions - Intel Community**

My solutions for the Altera DE1's labs in VHDL. Contribute to jprimeau/altera\_de1\_labs development by creating an account on GitHub.

## **GitHub - jprimeau/altera\_de1\_labs: My solutions for the ...**

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Labs Solution development boards by distinguishing itself with an abundance of interfaces to accommodate various application needs.

Extending its leadership and success, Terasic announces the latest DE2-115 that features the Cyclone® IV E device. Altera DE2-115 Development and Education Board The purpose of the

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## **Altera De2 Labs Solution - weer-en- wind.nl**

Get free lab exercises and solutions for semester-long courses on key engineering topics. Adapt and modify these assignments as needed to fit your course objectives and to gradually expose students to increasingly complex topics. All labs are

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offered in both Verilog and VHDL. Digital Logic.

## **Teach | Intel® FPGA Academic Program | Intel® Software**

The purpose of the Altera DE2 Development and Education board is to provide the ideal vehicle for advanced design prototyping in the multimedia, storage, and networking. It uses the

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state-of-the-art technology in both hardware and CAD tools to expose designers to a wide range of topics. The board offers a rich set of features that make it suitable for use in a laboratory environment for university and college courses, for a variety of design projects, as well as for the development of ...

**Terasic - Phased Out**  
*Page 20/29*

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## - **Main Boards -**

### **Altera DE2 Board**

Altera provides a number of ready-to-teach laboratory exercises for digital logic courses. Both Verilog and VHDL versions of exercises are provided. The exercises begin with fundamental concepts and show simple operations on the DE2/DE1 boards, such as using switches and controlling LEDs and

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seven-segment  
displays.

## **Altera's Laboratory Exercises | luugu**

Solution to Design  
Problem; ... If you look  
on the DE2 board, you  
will notice that this is  
the name and device of  
the Altera chip. The  
window should look like  
this. Proceed thru the  
last to windows (4 and  
5) and using the  
default settings. ... The  
complete pin listing for

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all the DE2 IO devices is provided on the lab web page under the commonly ...

## **Quartus Software Tutorial**

Extending its leadership and success, Terasic announces the latest DE2-115 that features the Cyclone IV E device. Responding to increased versatile low-cost spectrum needs driven by the demand

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for mobile video, voice, data access, and the hunger for high-quality images, the new DE2-115 offers an optimal balance of low cost, low power and a rich ...

## **Terasic - All FPGA Boards - Cyclone IV - Altera DE2-115 ...**

1. Create a new Quartus II project for your circuit. Select Cyclone II EP2C35F672C6 as the



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target chip, which is the FPGA chip on the Altera DE2 board. 2. Create a Verilog module for the code in Figure 1 and include it in your project. 3. Include in your project the required pin assignments for the DE2 board, as discussed above. 4. Compile the project. 5.

## **Part I The DE2 Board Provides 18 Toggle Switches ...**

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The new donation includes Terasic DE2-115 development boards and the Altera Quartus development software. "The DE2-115 is an incredibly flexible instructional platform that will enable us to create interesting, relevant, and highly motivating experiments for the students," said ECE associate professor Patrick Schaumont,

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who is teaching the courses. "This donation is helping us raise the bar in the quality of our program, and give our undergraduates meaningful experience with state-of-the-art ...

## **Altera donates FPGA platform for computer engineering labs ...**

Altera, the stylized Altera logo, specific device designations, and all other words and

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