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Calibre SvrF

Calibre InRoute uses true Calibre SVRF rule files and DFM models for DRC and DFM analysis within the Nitro-SoC environment. Calibre InRoute reduces the number of iterations required to achieve manufacturing signoff, minimizes engineering cost and enables faster time to market.

Calibre InRoute - True Manufacturing Signoff in Physical

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SVRF/TVF Technology - Mentor Graphics

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This is a syntax highlight file for Mentor Graphics Physical Extraction and Verification tool suite, Calibre. It highlights Calibre's rules language SVRF - Standard. Anyone who have a copy of "Standard Verification Rule Format (SVRF) Manual" for Calibre Verification?

CALIBRE SVRF PDF

The Calibre library contains a collection of learning paths that will help you master using the tools and the development of application-specific code.

Calibre - Mentor Graphics

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PVS DRC using SVRF (debug help) - Custom IC Design ...

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IC Design | Mentor Graphics Communities

SVRF is a CRF based extension for SVM (Chi-Hoon et al. SVRF may be mathematically represented using equation (2) where $O([y.sub.i], i(X))$ is an SVM-based Observation-Matching potential and $V([y.sub.i], [y.sub.j], X)$ is a (modified) DRF pair wise potential:

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